FMC5030 SERIES USER MANUAL

70 MHz to 18 GHz FMC Transceiver

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1. Introduction

1.1. Overview

Amtery FMC5030 is a 70 MHz – 18 GHz transceiver FMC board. It is equipped with:

- Analog Devices AD9364 transceiver, which covers 70 MHz 6 GHz.
- Upconverter and downconverter extending the frequency to cover 6 18 GHz.
- Internal or external reference clock.

The block diagram is shown below.



Figure 1. Block diagram

1.2. Compatible FPGA Carrier Board

The boot file in SD card supports Digilent ZedBoard. Please contact us if another FPGA carrier board's boot file is required.

1.3. Shipped Content

- a. Transceiver FMC board
- b. SD card with:
 - Analog Devices Kuiper Linux
 - Python and LabVIEW examples

1.4. Installation and Getting Started

Use Zedboard as host to control FMC5030



- a. Connect keyboard, mouse, monitor and power supply to ZedBoard.
- b. Insert SD card into ZedBoard and switch on power.
- c. In Kuiper Linux, execute "Application menu \rightarrow Other \rightarrow ADI IIO Oscilloscope" to control AD9364 onboard.
- d. To control RF front end, execute "fmc5030 rst" to reset FMC5030.
- e. Refer to 2.1 for detail usage.

Use external host (PC or notebook) to control Zedboard and FMC5030



- a. Download and install LibIIO: Go to <u>analogdevicesinc/libiio</u> or Amtery website.
 Windows: Download and execute "libiio-0.25.gb6028fd-setup.exe".
 Linux: Download and install "libiio-0.25-gb6028fd-(corresponding OS).deb (or .rpm)" according to the operating system. Some necessary packages may need to be installed to successfully install LibIIO. If none of the versions suit the OS, follow the <u>build instruction</u> to build LibIIO.
- b. Install Python 3.6 or newer versions.
- c. Install the amtFMC5030 through pip or pip3. This will install the necessary libraries and examples. After installation, the examples are in ...\site-packages\amtFMC5030\examples. Or download from Amtery website or copy from SD card. To run the examples, packages matplotlib, scipy, and thread6 are required.

pip install amtFMC5030

d. Install "openSSH server".

Windows: Use commands below on PowerShell as administrator. Refer to <u>Microsoft's page</u> for detail.

```
Add-WindowsCapability -Online -Name OpenSSH.Client~~~~0.0.1.0
Add-WindowsCapability -Online -Name OpenSSH.Server~~~0.0.1.0
Start-Service sshd
Set-Service -Name sshd -StartupType 'Automatic'
if (!(Get-NetFirewallRule -Name "OpenSSH-Server-In-TCP" -ErrorAction
SilentlyContinue | Select-Object Name, Enabled)) { Write-Output
"Firewall Rule 'OpenSSH-Server-In-TCP' does not exist, creating it..."
New-NetFirewallRule -Name 'OpenSSH-Server-In-TCP' -DisplayName 'OpenSSH
Server (sshd)' -Enabled True -Direction Inbound -Protocol TCP -Action
Allow -LocalPort 22 } else { Write-Output "Firewall rule 'OpenSSH-
Server-In-TCP' has been created and exists." }
```

Linux: Install and start openssh-server:

sudo dnf/apt install openssh-server
sudo systemctl start ssh
service ssh status #check ssh status

- e. Make sure the ethernet connection between Zedboard and PC.
- f. To control RF front end, execute "amtFmcRfReset()" to remotely reset FMC5030.
- g. Refer to 2.2 for detail usage.

2. Use FMC5030

2.1. Control FMC5030 by Commands in ZedBoard Kuiper Terminal

FMC5030 can be controlled in Analog Devices Linux Kuiper terminal by using commands. The software architecture is in Figure 2. By this method, the tuner and AD9364 are controlled separately. The terminal commands control the tuner and the IIO's RX LO and TX LO frequency. The IIO controls the AD9364. When the RF frequency is over 6 GHz, the IIO's RX LO or TX LO frequency will be set to corresponding intermediate frequency, 440 MHz. Refer to Table 1 for the commands list.



Figure 2 Software architecture of controlling FMC5030 by terminal commands in Kuiper

Table 1 Terminal Commands

These commands are case-insensitive.

Command	Description	Parameters	Example
fmc5030	Show help for these commands.		
fmc5030 rst	Reset and initialize FMC5030. The		fmc5030 rst
	default settings are:		(Reset FMC5030.)
	Frequency: 9000 MHz		
	RxA att: 0		
fmc5030 freq [T/R] [frequency]	Set the TxA or RxA frequency. AD9364's	[port]	fmc5030 freq T 10100
	ports will be set to corresponding IF	TR: Set both TxA and RxA.	(Set TxA frequency to 10.1 GHz.)
	frequency.	T: Set TxA, ignore RxA.	
	(TxB and RxB are directly connected to	R: Set RxA, ignore TxA.	
	AD9364 and controlled in IIO.)	[frequency]	
		Frequency in MHz, 1 Hz resolution.	
fmc5030 att [value]	Set RxA attenuation.	[value]	fmc5030 att -15
		Attenuation in dB. 1 dB resolution.	(Set RxA attenuation to -15 dB.)
fmc5030 fv	Display firmware version.		fmc5030 fv

Terminal commands examples:

a. Acquire a signal at RxA, 0.1 GHz.

Terminal:

analog@analog:~/Public	\$ fmc5030 rst
Initialize channels.	
Reset FMC5030. TxA and	RxA frequency = 9000 MHz. RxA attenuation = 0 dB.
analog@analog:~/Public	\$ fmc5030 freq R 100
Use RxA bypass path.	

IIO Oscilloscope: The RX LO Frequency(MHz) is set to 100 by terminal command. Press "Reload Settings" to update the GUI.

		ADI II	O Oscilloscop	e			~ ^ >
File Settings Help							
AD936X Advanced 🐠 AD93	36X 🤹 DMM 🔮	Debug 🔮	Spectrum An	alyzer 🔮			
Controls Block Diagram							
AD9361 / AD9364 Rece	eive Chain						
RF Bandwidth(MHz): Sam	npling Rate (MSPS):	RX LO Freq	uency(MHz):	RF Port	Select	Fastlock Profile:	Tracking
18.000	720000	100.00000	0	A_BALANCE	ED 🗸	0 🗸	🗹 Quadra
		External	I RX LO			Store Recall	RF DC
-RX 1							BB DC
Hardware Gain(dB): 20.	.00						
RSSI(dB): 74.7	75 dB						
Gain Control: mar	nual						
Gain Control Modes: ma	anual 👻						
🔶 AD9361 / AD9364 Tran	nsmit Chain						
RF Bandwidth(MHz): Sam	npling Rate (MSPS):	TX LO Freq	uency(MHz):	RF Port Sele	ct Fastloc	k Profile:	
18.000	720000	100.00000	0	Α ,	• 0	-	
		Externa	I TX LO		Store	Recall	
TX 1 Attenuation(dB): 0.00							•
		Rel	oad Settings				•

b. Acquire a signal at RxA, 12 GHz with attenuation -10 dB.

Terminal:



IIO Oscilloscope: The RX LO Frequency(MHz) is set to the corresponding intermediate frequency, 440, by terminal command. Press "Reload Settings" to update the GUI.

			ADII	IO Oscilloscop	e				~	~ :
File Settings Help				1						
AD936X Advanced 垂	AD936X 🐠	DMM 🔮	Debug 🔮	Spectrum Ar	nalyzer 🔮					
Controls Block Diag	ram									
AD9361 / AD936	4 Beceive Cha	in								
RF Bandwidth(MHz)	: Sampling R	ate (MSPS):	RX LO Free	quency(MHz):	RF Por	t Select	Fastloc	k Profile:	Tracking	
18.000	30.720000	•	440.0000	00	A_BALANO	CED 🗸	0	•	🗹 Qua	adra
			Externa	al RX LO			Store	Recall	🗹 RF	DC
RX 1 Hardware Gain(df RSSI(df Gain Contro Gain Control Mode	3): 20.00 3): 65.00 dB ol: manual es: manual	* *							✓ BB	DC
AD9361 / AD936	4 Transmit Ch				DE Dort So	oct Eactloo	k Profilo			
18.000	30.720000		440.0000		A		k Piolile. ▼			
			Externa	al TX LO		Store	Recall			
Attenuation(dB): 0.0 RSSI(dB):	0									•
			Re	load Settings						

c. Transmit a signal at TxA, 1 GHz

Terminal:

```
analog@analog:~/Public $ fmc5030 rst
Initialize channels.
Reset FMC5030. TxA and RxA frequency = 9000 MHz. RxA attenuation = 0 dB.
analog@analog:~/Public $ fmc5030 freq T 1000
Use TxA bypass path.
```

IIO Oscilloscope: The TX LO Frequency(MHz) is set to 1000 by terminal command. Press "Reload Settings" to update the GUI.

			ADII	O Oscilloscop)e				× ^
File Settings Help									
AD936X Advanced 🥌	AD936X 🐠	DMM 🔮	Debug 🐠	Spectrum Ar	nalyzer 📧				
Controls Block Diagr	am								
🔶 AD9361 / AD936	4 Receive Cha	in							
RF Bandwidth(MHz)	Sampling Ra	ate (MSPS)	RX LO Free	quency(MHz): 000	RF Por	rt Select CED 🗸	Fastloc 0	k Profile: •	Tracking ✓ Quadra
			Externa	al RX LO			Store	Recall	RF DC
Hardware Gain(dE RSSI(dE Gain Control Gain Control Mode	3): 20.00 3): 51.25 dB bl: manual s: manual	÷							
🔶 AD9361 / AD936	4 Transmit Ch	ain							
RF Bandwidth(MHz)	30.720000	ate (MSPS)	TX LO Free 1000.0000	juency(MHz):	RF Port Se A	lect Fastloc	k Profile: •		
			Externa	al TX LO		Store	Recall		
TX 1 Attenuation(dB): 0.0 RSSI(dB):	0								
			Re	load Settings					•

d. Transmit a signal at TxA, 18 GHz

Terminal:



IIO Oscilloscope: The TX LO Frequency(MHz) is set to the corresponding intermediate frequency, 440, by terminal command. Press "Reload Settings" to update the GUI.

		ADII	IO Oscilloscop	e				× ^
ile Settings Help								
AD936X Advanced 🔮 AD936X 🔹	DMM 🔮	Debug 🔮	Spectrum Ar	nalyzer 🔮				
Controls Block Diagram								
AD9361 / AD9364 Receive Ch	ain							
RF Bandwidth(MHz): Sampling	Rate (MSPS)	RX LO Free	quency(MHz):		t Select	Fastloo	k Profile:	Tracking
• 00.72000	*	Externa	al RX LO	ALDALAIN	JLD ¥	Store	Recall	RF DC
RX 1 Hardware Gain(dB): 20.00 RSSI(dB): 53.75 dB Gain Control: manual Gain Control Modes: manual	* *							SP DC
🔶 AD9361 / AD9364 Transmit (hain							
RF Bandwidth(MHz): Sampling 18.000 30.720000	Rate (MSPS)	TX LO Free	quency(MHz): 00	RF Port Se A	ect Fastloc	k Profile		
		Externa	al TX LO		Store	Recall		
-TX 1 Attenuation(dB): 0.00 RSSI(dB):								
		Re	load Settings					•

2.2. Remotely Control ZedBoard and FMC5030 From External Host

Python libraries are provided to control FMC5030. The program is executed in an external host computer. By this method, python or LabVIEW programs can control the tuner and AD9364 simultaneously. Refer to 1.4 for examples positions.



Table 2 Python and LabVIEW API

Function	Description	Parameters
amtFmcRfReset(ip: str, port: int,	Reset and initialize FMC5030. Return a	ip: ZedBoard's IP. Defualt: 169.254.92.202
username: str, password: str) ->	reference, which denotes the connection	port: Zedboard's port for SSH. Defualt: 22
reference: list	between host and device. The default	username: Zedboard's username. Defualt: analog
	settings are:	password: Zedboard's password to the user. Defualt: analog
	Frequency: 9000 MHz	reference: The connection between host and device.
	RxA att: 0 dB	

dof amtEmcTyConfig(rfDorts str	Config Ty parameters for transmission	rfDort: Solast transmission port. A or D. TvA's fraguancy range is 70 MHz to
	Comig ix parameters for transmission.	Theory select transmission port, A or B. TXA's frequency range is 70 MHz to
frequency: int, rate: int, bw: int,		18 GHz, TxB is 70 MHz to 6 GHz.
txAtt: int, cyclic: bool)		frequency: Frequency in MHz.
		rate (optional): IQ rate in MSPS. Default value: 30.72 MSPS. AD9364's Tx and
		Rx share the same IQ rate. This value sets Rx rate simultaneously.
		bw (optional): Set AD9364's internal analog filter bandwith in MHz. Default
		value: 18 MHz.
		txAtt (optional): AD9364's Tx attenuation in dB. Range: 0 to -89 dB. Default
		value: 0 dB.
		cyclic (optional): True or false. If true, the data in buffer will be transmitted
		repeatedly. If false, the data in buffer will be transmitted only once.
amtFmcRfTxStart (data: complex)	Start transmitting data.	data: Transmitted data.
amtFmcRfTxStop ()	Stop transmitting.	
amtFmcRxConfig(rfPort: str,	Config Rx parameters for receiving.	rfPort: Select receiving port, A or B. RxA's frequency range is 70 MHz to 18
frequency: int, rate: int, bw: int,	Currently FMC5030 Rx doesn't support	GHz, RxB is 70 MHz to 6 GHz.
numOfSamples: int, rxAAtt: int,	cyclic buffer. There are data gaps	frequency: Frequency in MHz.
rxGain: int)	between each section of receiving data.	rate (optional): IQ rate in MSPS. Default value: 30.72 MSPS. AD9364's Tx and
		Rx share the same IQ rate. This value sets Tx rate simultaneously.
		bw (optional): Set AD9364's internal analog filter bandwith in MHz. Default
		value: 18 MHz.
		numOfSamples (optional): Number of IQ samples. Default value: 16384.
		rxAAtt (optional): RxA attenuation value in dB. Range: 0 to -31 dB. Default
		value: 0 dB.
		rxGain (optional): AD9364's Rx gain in dB. Range: 0 to 70 dB. Default value: 0
		dB.
amtFmcRfRxRead () -> data: float	Receive one section of data.	data: Received data. Row 0 stores the real part, and row 1 stores the
	Currently FMC5030 Rx doesn't support	imaginary part.

	cyclic buffer. There are data gaps	
	between each section of receiving data.	
amtFmcRfFv ()	Display FMC5030 firmware version.	
amtFmcRfRef (ip: str, refClk: str,	Set internal or external reference clock	ip: Zedboard's IP.
clkFreq: float)	and frequency. Refer to 2.3 for detail	refClk: Select internal or external reference clock. "int" to use internal
	usage.	reference clock and "ext" to use external reference clock.
		clkFreq: External reference clock frequency. Range: 10 to 100 MHz. Ignored
		if internal reference clock is used.

2.3. Use external or internal reference clock

Once the reference clock is set to external or internal, it sustains until further change. Please note that the reference clock can't be set in programs dynamically due to reboot is necessary.

2.3.1. Use external reference clock

The external reference clock can be sine or square wave with frequency range 10 MHz to 100 MHz. It can be set in Kupier Linux terminal or by Python command in external PC.

Zedboard Kupier Linux Terminal

1. Execute "amtFmcRfRef ext (clock frequency in MHz)" as root.



2. Reboot the device.

Python in external PC

1. Use function "amtFmcRfRef (ip,refClk,clkFreq)".

```
# Zedboard's IP
ip = "192.168.1.193"
# Input "int" to use internal clock and "ext" to use external reference clock.
refClk = "ext"
# clkFreq: Reference clock frequency in MHz, range: 10 to 100 MHz.
clkFreq = 10
# Note that 10 MHz reference clock is required to be a sinusoidal wave.
fmc.amtFmcRfRef(ip,refClk,clkFreq)
```

Output:

Linux analog 5.10.0-98248-g1bbe32fa5182 #1142 SMP PREEMPT Wed Aug 3 The programs included with the Debian GNU/Linux system are free soft the exact distribution terms for each program are described in the individual files in /usr/share/doc/*/copyright. Debian GNU/Linux comes with ABSOLUTELY NO WARRANTY, to the extent permitted by applicable law. Last login: Thu Nov 23 08:45:44 2023 from 192.168.1.86 su analog@analog:~\$ su Password: root@analog:/home/analog# cd /home/analog/Public root@analog:/home/analog/Public# ./amtFmcRfRef.sh ext 10 Rebuild devicetree... CURRENT BOARD CONFIG: DONE Reference clock = external. Clock frequency = 10 MHz.

2. Reboot the device.

2.3.2. Use internal reference clock

The internal reference clock can also be set in Kupier Linux terminal or by Python command in external PC.

Zedboard Kupier Linux Terminal

1. Execute "amtFmcRfRef int" as root.



2. Reboot the device.

Python in external host

1. Use function "amtFmcRfRef (ip,refClk,clkFreq)". Note that clkFreq is ignored.

```
# Zedboard's IP
ip = "192.168.1.193"
# Input "int" to use internal clock and "ext" to use external reference clock.
refClk = "int"
# Note that 10 MHz reference clock is required to be a sinusoidal wave.
fmc.amtFmcRfRef(ip,refClk)
```

Output:

Linux analog 5.10.0-98248-g1bbe32fa5182 #1142 SMP PREEMPT Wed Aug 3 The programs included with the Debian GNU/Linux system are free soft the exact distribution terms for each program are described in the individual files in /usr/share/doc/*/copyright. Debian GNU/Linux comes with ABSOLUTELY NO WARRANTY, to the extent permitted by applicable law. Last login: Thu Nov 23 09:07:48 2023 su analog@analog:~\$ su Password: root@analog:/home/analog# cd /home/analog/Public root@analog:/home/analog/Public# ./amtFmcRfRef.sh int 80 Rebuild devicetree... CURRENT BOARD CONFIG: DONE Reference clock = internal. Clock frequency = 80 MHz.

2. Reboot the device.